Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	radix near search\$3 and (lookup\$1 or reference\$1) same index\$3 and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 14:11
L2	5	radix near search\$3 and index\$3 and transition\$1 same bit\$1 same offset\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 14:12
S1	2	("6633880").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:44
S2	2	("5774739").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:46
S3	2	("6185570").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:47
S4	2	("5873078").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:51
S5	9075	707/2,102,104.1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 11:52
S6	2	S5 and radix near search\$3 and index\$3 same reference near table\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 11:54
S7	3	707/3,101.ccls. and radix near search\$3 and index\$3 same reference near table\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:07
S8	5	radix near search\$3 and index\$3 same reference near table\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:09

S9	10	radix near search\$3 and index\$3 same bit\$1 same key\$1 and table\$1 with (transition\$1 or valid\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:10
S10	0	radix near search\$3 and index\$3 same base near address\$1 same (previous or old or prior) near index\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:11
S11	2	radix near search\$3 and index\$3 same (base\$1 or combin\$6) near address\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:12
S12	11	radix near search\$3 and index\$3 and result\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:14
S13	31	radix near search\$3 and index\$3 and (refence\$3 or lookup or look-up or pointer\$1) same bit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:46
S14	10	radix near search\$3 and index\$3 and (valid or validit\$3) near bit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:46
S15	11	radix near search\$3 and index\$3 and (valid or validit\$3) same bit\$1 same key\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:49
S16	1	radix near search\$3 and index\$3 and (offset with pointer\$1) and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 14:03
S17	1	search\$3 and index\$3 same bit\$1 and (offset with pointer\$1) and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:51
S18	1	search\$3 and index\$3 same bit\$1 and (offset same pointer\$1) and ((valid or validit\$3) near table\$1) and transition\$1 near table\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/08 12:51

Day: Thursday Date: 12/8/2005





PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = MODELSKI First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09741845	Not Issued	71	12/22/2000	Route switch packet architecture	MODELSKI, RICHARD P.
09741846	6981077	150	12/22/2000	GLOBAL ACCESS BUS ARCHITECTURE	MODELSKI, RICHARD P.
09741852	6772268	150	12/22/2000	CENTRALIZED LOOK UP ENGINE ARCHITECTURE AND INTERFACE	MODELSKI, RICHARD P.
09741853	Not Issued	61	12/22/2000	Method and apparatus for monitoring a network using statistical information stored in a memory entry	MODELSKI, RICHARD P.
<u>09741855</u>	6684300	150	12/22/2000	EXTENDED DOUBLE WORD ACCESSES	MODELSKI, RICHARD P.
09741856	Not Issued	71	12/22/2000	Method and apparatus for performing filter operations on data packets using an instruction	MODELSKI, RICHARD P.
09741857	Not Issued	41	12/22/2000	Multi-thread packet processor	MODELSKI, RICHARD P.
<u>09741858</u>	<u>6847640</u>	150	12/22/2000	HIGH SPEED PHYSICAL CIRCUITS OF MEMORY INTERFACE	MODELSKI, RICHARD P.
09741859	6633880	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	MODELSKI, RICHARD P.
09741999	Not Issued	93	12/22/2000	BIT FIELD MANIPULATION	MODELSKI, RICHARD P.
09742286	6665755	150	12/22/2000	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	MODELSKI, RICHARD P.
09742287	Not Issued	61	12/22/2000	Bi-directional access port architecture	MODELSKI, RICHARD P.
09742288	Not Issued	71	12/22/2000	Load-shift carry instruction	MODELSKI, RICHARD P.
09742289	Not	161	12/22/2000	Method and apparatus for key	MODELSKI,

	Issued			hashing based on a programmable hash function	RICHARD P.
09742290	Not Issued	71		1	MODELSKI, RICHARD P.
09742300	<u>6778526</u>	150		HIGH SPEED ACCESS BUS INTERFACE AND PROTOCOL	MODELSKI, RICHARD P.
10678677	6934780	150	10/03/2003		MODELSKI, RICHARD P.

Inventor Search Completed: No Records to Display.

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = CRAREN First Name = MICHAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09741845</u>	Not Issued	71	12/22/2000	Route switch packet architecture	CRAREN, MICHAEL J.
<u>09741846</u>	6981077	150	12/22/2000	GLOBAL ACCESS BUS ARCHITECTURE	CRAREN, MICHAEL J.
09741856	Not Issued	71	12/22/2000	Method and apparatus for performing filter operations on data packets using an instruction	CRAREN, MICHAEL J.
09741857	Not Issued	41	12/22/2000	Multi-thread packet processor	CRAREN, MICHAEL J.
09741859	6633880	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	CRAREN, MICHAEL J.
09741999	Not Issued	93	12/22/2000	BIT FIELD MANIPULATION	CRAREN, MICHAEL J.
09742286	6665755	150	12/22/2000	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	CRAREN, MICHAEL J.
09742288	Not Issued	71	12/22/2000	Load-shift carry instruction	CRAREN, MICHAEL J.
09742290	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	CRAREN, MICHAEL J.
09931643	Not Issued	80	08/15/2001	Optical switch router	CRAREN, MICHAEL J.
10678677	6934780	150	10/03/2003	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	CRAREN, MICHAEL J.
60225594	Not Issued	159	08/15/2000	Optical internet protocol switch router	CRAREN, MICHAEL J.

Inventor Search Completed: No Records to Display.

Last Name First Name Search Another: Inventor CRAREN MICHAEL

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Inventor Name Search Result

Your Search was:

Last Name = KRISTIANSEN

First Name = ADRIAN

Application#	Patent#	Status	Data Filad	Title	Inventor Name
09741852	6772268	150	12/22/2000	CENTRALIZED LOOK UP ENGINE ARCHITECTURE AND INTERFACE	KRISTIANSEN, ADRIAN
09741853	Not Issued	61	12/22/2000	Method and apparatus for monitoring a network using statistical information stored in a memory entry	KRISTIANSEN, ADRIAN M.
09741856	Not Issued	71	12/22/2000	Method and apparatus for performing filter operations on data packets using an instruction	KRISTIANSEN, ADRIAN M.
09741859	6633880	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	KRISTIANSEN, ADRIAN M.
09742286	6665755	150	12/22/2000	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	KRISTIANSEN, ADRIAN M.
09742289	Not Issued	161	12/22/2000	Method and apparatus for key hashing based on a programmable hash function	KRISTIANSEN, ADRIAN M.
09742290	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	KRISTIANSEN, ADRIAN M.
10678677	6934780	150	10/03/2003	EXTERNAL MEMORY ENGINE SELECTABLE PIPELINE ARCHITECTURE	KRISTIANSEN, ADRIAN M.

Inventor Search Completed: No Records to Display.

Last Name First Name Search Another: Inventor

KRISTIANSEN ADRIAN Search

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ANGLE First Name = RICHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08490983	<u>5541920</u>	150	06/15/1995	METHOD AND APPARATUS FOR A DELAYED REPLACE MECHANISM FOR A STREAMING PACKET MODIFICATION ENGINE	ANGLE, RICHARD
<u>09108468</u>	6185570	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION BITS AND FIELDS IN TRANSITION TABLES	ANGLE, RICHARD
09108469	6247014	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING HASH LOOKUPS USING VALID BIT TABLES WITH POINTERS	ANGLE, RICHARD
09108711	6223174	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING VALID BIT TABLES WITH POINTERS	ANGLE, RICHARD
09108751	6233574	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION TABLES WITH POINTERS	ANGLE, RICHARD
09309471	6601150	150		MEMORY MANAGEMENT TECHNIQUE FOR MAINTAINING PACKET ORDER IN A PACKET PROCESSING SYSTEM	ANGLE, RICHARD
09741859	6633880	150	4	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	ANGLE, RICHARD L.
09742290	Not Issued	71	12/22/2000	Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	ANGLE, RICHARD L.
10234689	6771596	150		BACKPRESSURE MECHANISM FOR A NETWORK DEVICE	ANGLE, RICHARD L.
10242259	Not	71	09/11/2002	Multicast and unicast scheduling	ANGLE, RICHARD

	Issued	i		for a network device	L
07482245	Not Issued	161	02/20/1990	MULTIPROCESSOR COMPUTER SYSTEM WITH EACH PROCESSOR HAVING A BI-LEVEL CACHE	ANGLE, RICHARD L.
08684224	5873078	150	07/19/1996	RADIX TREE SEARCH LOGIC	ANGLE, RICHARD L.
08690225	5857196	150	07/19/1996	METHOD FOR STORING A TREE OF POTENTIAL KEYS IN A SPARSE TABLE	ANGLE, RICHARD L.
08708895	6226687	150	09/05/1996	METHOD AND APPARATUS FOR MAINTAINING AN ORDER OF DATA PACKETS	ANGLE, RICHARD L.
08710736	5774739	150	09/20/1996	USING A LOOKUP PROCESSOR TO SEARCH A TABLE OF KEYS WHOSE ENTIRES CONTAIN INSTRUCTION POINTER VALUES OF CODE TO EXECUTE IF KEY IS FOUND	ANGLE, RICHARD L.
08716989	6209020	250	09/20/1996	DISTRIBUTED PIPELINE MEMORY ARCHITECTURE FOR A COMPUTER SYSTEM	ANGLE, RICHARD L.
08717323	5761506	150	09/20/1996	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	ANGLE, RICHARD L.
08717404	5848257	150	09/20/1996	METHOD AND APPARATUS FOR MULTITASKING IN A COMPUTER SYSTEM	ANGLE, RICHARD L.
09062393	6272516	150	II I	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	ANGLE, RICHARD L.
09062787	5935235	150	04/16/1998	METHOD FOR BRANCHING TO AN INSTRUCTION IN A COMPUTER PROGRAM AT A MEMORY ADDRESS POINTED TO BY A KEY IN A DATA STRUCTURE	ANGLE, RICHARD L.
09311833	6628646	150	05/14/1999	PROGRAMMABLE MULTICAST SCHEDULING FOR A NETWORK DEVICE	ANGLE, RICHARD L.
09311834	6477169	150		MULTICAST AND UNICAST SCHEDULING FOR A NETWORK DEVICE	ANGLE, RICHARD L.
09311942	6661788	150	05/14/1999	MULTICAST SCHEDULING FOR A NETWORK DEVICE	ANGLE, RICHARD L.
09311995	6519225	150	05/14/1999	BACKPRESSURE MECHANISM	ANGLE, RICHARD

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				FOR A NETWORK DEVICE	L
09434159	6633576	150	11/04/1999	APPARATUS AND METHOD FOR INTERLEAVED PACKET STORAGE	ANGLE, RICHARD L.
09457508	6422876	150	12/08/1999	HIGH THROUGHPUT INTERCONNECTION SYSTEM USING ORTHOGONAL CONNECTORS	ANGLE, RICHARD L.
06402415	4466321	150	07/28/1982	SCRAGG MILL	ANGLE, RICHARD O.
09574865	6726400	150	31	VEHICLE ARRESTING BED SYSTEMS	ANGLEY, RICHARD D.
09951080	6685387	150	09/13/2001	JET BLAST RESISTANT VEHICLE ARRESTING BLOCKS, BEDS AND METHODS	ANGLEY, RICHARD D.
10755163	6971817	150	01/09/2004	JET BLAST RESISTANT VEHICLE ARRESTING BLOCKS, BEDS AND METHODS	ANGLEY, RICHARD D.
11253333	Not Issued	30	10/19/2005	Jet blast resistant vehicle arresting blocks, beds and methods	ANGLEY, RICHARD D.
<u>08796968</u>	5789681	150	02/07/1997	ARRESTING MATERIAL TEST APPARATUS AND METHODS	ANGLEY, RICHARD D.
08796969	5902068	150	02/07/1997	VEHICLE ARRESTING UNIT FABRIICATION METHODS	ANGLEY, RICHARD D.
08796970	5885025	150	02/07/1997	VEHICLE ARRESTING BED SYSTEMS	ANGLEY, RICHARD D.
09259730	Not Issued	168		VEHICLE ARRESTING BED SYSTEMS	ANGLEY, RICHARD D.

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = LADWIG First Name = GEOFF

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08902396	6047304	150	07/29/1997	METHOD AND APPARATUS FOR PERFORMING LANE ARITHMETIC TO PERFORM NETWORK PROCESSING	LADWIG, GEOFF
<u>09108468</u>	6185570	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION BITS AND FIELDS IN TRANSITION TABLES	LADWIG, GEOFF
09108469	6247014	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING HASH LOOKUPS USING VALID BIT TABLES WITH POINTERS	LADWIG, GEOFF
<u>09108711</u>	6223174	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING VALID BIT TABLES WITH POINTERS	LADWIG, GEOFF
09108751	6233574	150	07/01/1998	METHOD AND APPARATUS FOR PERFORMING RADIX LOOKUPS USING TRANSITION TABLES WITH POINTERS	LADWIG, GEOFF
60137201	Not Issued	159	06/02/1999	HIGH THROUGHPUT CHASSIS INTERCONNECTION SYSTEM	LADWIG, GEOFF
09741859	6633880	150	12/22/2000	METHOD AND APPARATUS FOR PERFORMING DISTINCT TYPES OF RADIX SEARCHES	LADWIG, GEOFF B.
09742290	Not Issued	71		Method and apparatus for performing a radix search by selecting one of a valid table and a transition table	LADWIG, GEOFF B.
08490983	5541920	150		METHOD AND APPARATUS FOR A DELAYED REPLACE MECHANISM FOR A STREAMING PACKET	LADWIG, GEOFFREY

				MODIFICATION ENGINE	1
08710736	5774739	150	09/20/1996	USING A LOOKUP PROCESSOR TO SEARCH A TABLE OF KEYS WHOSE ENTIRES CONTAIN INSTRUCTION POINTER VALUES OF CODE TO EXECUTE IF KEY IS FOUND	LADWIG, GEOFFREY B
09863043	Not Issued	161	05/22/2001	Communication channel optimization using forward error correction statistics	LADWIG, GEOFFREY B.
10234689	6771596	150	09/04/2002	BACKPRESSURE MECHANISM FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
10242259	Not Issued	71	09/11/2002	Multicast and unicast scheduling for a network device	LADWIG, GEOFFREY B.
08684224	5873078	150	07/19/1996	RADIX TREE SEARCH LOGIC	LADWIG, GEOFFREY B.
08690225	5857196	150	07/19/1996	METHOD FOR STORING A TREE OF POTENTIAL KEYS IN A SPARSE TABLE	LADWIG, GEOFFREY B.
08708895	6226687	150	09/05/1996	METHOD AND APPARATUS FOR MAINTAINING AN ORDER OF DATA PACKETS	LADWIG, GEOFFREY B.
08716989	6209020	250	09/20/1996	DISTRIBUTED PIPELINE MEMORY ARCHITECTURE FOR A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
08717323	5761506	150		METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
08717404	5848257	150	09/20/1996	METHOD AND APPARATUS FOR MULTITASKING IN A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
09062393	6272516	150	04/17/1998	METHOD AND APPARATUS FOR HANDLING CACHE MISSES IN A COMPUTER SYSTEM	LADWIG, GEOFFREY B.
<u>09062787</u>	5935235	150	1.1	METHOD FOR BRANCHING TO AN INSTRUCTION IN A COMPUTER PROGRAM AT A MEMORY ADDRESS POINTED TO BY A KEY IN A DATA STRUCTURE	LADWIG, GEOFFREY B.
09309471	6601150	150	05/10/1999	MEMORY MANAGEMENT TECHNIQUE FOR MAINTAINING PACKET ORDER IN A PACKET PROCESSING SYSTEM	LADWIG, GEOFFREY B.

09311833	6628646	150		PROGRAMMABLE MULTICAST SCHEDULING FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
09311834	6477169	150	05/14/1999	MULTICAST AND UNICAST SCHEDULING FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
09311942	6661788	150	05/14/1999	MULTICAST SCHEDULING FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
09311995	6519225	150		BACKPRESSURE MECHANISM FOR A NETWORK DEVICE	LADWIG, GEOFFREY B.
09434159	6633576	150		APPARATUS AND METHOD FOR INTERLEAVED PACKET STORAGE	LADWIG, GEOFFREY B.
09457508	6422876	150		HIGH THROUGHPUT INTERCONNECTION SYSTEM USING ORTHOGONAL CONNECTORS	LADWIG, GEOFFREY B.

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